Patents Patents **1** - **10** on **performed multi level access based upon I/O space access**. (**0.07** seconds)

[APPLICATION] Method and apparatus for **multi**-table accessing of input/output devices using ...
US Pat. 10047188 - Filed Jan 15, 2002
The **multi**-table **I/O space access performed** by the system 200 is described in greater detail below. **Based upon** the security **level** that is established and the **...**

[APPLICATION] Trusted client utilizing security kernel under secure execution mode
US Pat. 10160984 - Filed May 31, 2002
The **multi**-**level I/O access** table system provided by embodiments of the present **... based upon** a virtual **I/O** address generated by the **I/O access** table 2010. **...**

System, apparatus and method for **multi**-**level** cache in a **multi**-processor ...
US Pat. 6460122 - Filed Sep 30, 1999 - International Business Machine Corporation
This is done by adjusting the **level** of the 60 cache **based upon** how the host **...** be kept in the **level** 0 cache if **space** is available to allow fast **access** by **...**

System with real-time checking of privilege levels and the system's state to ...
US Pat. 5043878 - Filed Aug 9, 1989 - NEC Corporation
On the other hand, the task context switch dress conversion for the **I/O space** so as to **access** the instruction is an instruction for replacement of the con- **...**

Input/output control technique utilizing multilevel memory structure for ...
US Pat. 4783730 - Filed Sep 19, 1986 - Datapoint Corporation
However, in the case of more complex **I/O** Devices, there is a need for more **...** every other module and **access** the Mailbox associated with every other module. **...**

Memory-**based** interagent communication mechanism
US Pat. 4829425 - Filed Mar 1, 1988 - Intel Corporation
The local address **space** provides **access** to several This mechanism is provided to control **access** to register types of storage, including special **I/O ...**

System for delaying processing of memory **access** exceptions until the ...
US Pat. 4985825 - Filed Feb 3, 1989 - Digital Equipment Corporation
At each of these translation hand, if an **access** request is denied, **...** not disrupt the are generally **based** on the concept of **multi**-processing execution of **...**

Multi-tasking register set mapping system which changes a register set ...
US Pat. 4853849 - Filed Dec 17, 1986 - Intel Corporation
The **ACCESS** in- 10 The MOVE POINTER instruction allows register struction **... I/O space** The address **space** on the **I/O** bus side of the CP is 15 Register set **...**

Method for pipeline processing of instructions by controlling **access** to a ...
US Pat. 5721855 - Filed Jul 12, 1996 - Intel Corporation
34, a computer system 3400, L2 cache 156 **access**, thereby permitting overlap **...** The **I/O** units collect interrupts from a variety of sources, includes a **level ...**

Method to control **I/O** accesses in a **multi**-tasking virtual memory virtual ...

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